



ಬಿ.ಎಂ.ಎಸ್. ತಾಂತ್ರಿಕ ಮತ್ತು ವ್ಯವಸ್ಥಾಪನಾ ಮಹಾವಿದ್ಯಾಲಯ

BMS Institute of Technology and Management

(An Autonomous Institution Affiliated to VTU, Belagavi)
Avalahalli, Doddaballapur Main Road, Bengaluru – 560119

Department of Electronics and Communication Engineering

M.Tech.-VLSI System Design (VSD)

Expert Talk Report: Cyril Prasanna

Date: January 24, 2026

Speaker: Dr. Cyril Prasanna

Mr. Cyril Prassanna delivered an engaging expert talk focused on practical IC design concepts. The session emphasized activity-based learning and provided valuable industry insights.

Analog-Digital Communication

The talk began with a clear overview of how analog signals communicate with digital systems in real-world applications. He explained mixed-signal interfaces using everyday examples like audio processing and sensor data conversion. He highlighted challenges in signal integrity and the role of ADCs/DACs in bridging these domains.

Student Tapeouts and Packaging Careers

Mr. Prasanna shared impressive tapeout achievements by his students, detailing successful silicon implementations. He showcased real project outcomes and lessons learned from fabrication runs. Additionally, he presented emerging career opportunities in IC packaging, including roles in 2.5D/3D integration and advanced packaging technologies.

IC Core Architecture

The speaker explained the core structure of an integrated circuit, covering die layout, power distribution, and block partitioning. He broke down hierarchical design principles relevant to modern SoCs. To reinforce concepts, Prasanna conducted hands-on activities during the session itself.

RTL to GDSII Design Flow

Mr. Prasanna walked through the complete RTL to GDSII flow with practical examples for each stage, starting by choosing the Foundry, the standard library, then specifications and so on.

He placed special emphasis on synthesis, discussing constraints, area recovery, and power optimization techniques.

Session Impact

The activity-based approach made complex VLSI concepts accessible and memorable. Participants gained practical insights into industry workflows and career paths. Overall, the session was highly beneficial for VLSI students pursuing design and tapeout projects.



PG-VSD Coordinator
Dr. Banuprakash R



HoD ECE
Dr. A Shobha Rani